

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:)	
Savekar, et al.)	Electronically Filed:
)	
Serial No.: 10/725,974)	Date: April 22, 2008
)	
Filed: December 02, 2003)	
)	
For: "VIDEO DISPLAY AND DECODE)	
UTILIZING OFF-CHIP PROCESSOR)	
AND DRAM")	
)	
Group Art Unit: 2672)	
)	
Examiner: Tayong)	
)	
Confirmation No.: 4960)	

REQUEST FOR PRE-APPEAL BRIEF REVIEW

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

This correspondence is filed in response to the Final
Office Action of January 22, 2008.

REMARKS

Claims 1-18 are presently pending and stand rejected. Claims 19-21 are cancelled without prejudice. Pre-appeal brief review of the rejections to claims 1, 7, and 13 are respectfully requested.

Claims 1-6, 11-12, and 13-18 were rejected under 35 U.S.C. § 102(b) as being anticipated by Kono. Claim 1 recites, among other limitations, "a display manager for determining when to overwrite an existing image in the image buffers, and providing a signal to the decoder indicating when to overwrite the existing image in the frame buffer".

Examiner has indicated that Kono in "(figure 6, 55) discloses a display control section that analyzes the content of the parameters that have been read, and determines how to display the picture (step S15 in figure 7). On (page 6, [0080]-[0061]), Kono further disclosed that parameters of each layer that have been generated as a result of the decoding are overwritten into the parameters stored in the MB buffer 58 (step s6 figure 70. Then the parameters are transferred from the MB buffer to the parameter bank, and are written into this parameter bank."

Assignee respectfully submits that "overwrite an existing image" does not read on "Parameters of each layer that have been generated as a result of the decoding are overwritten". Accordingly, Assignee respectfully traverses the rejections to claims 1 and 13 for this reason alone.

Moreover, even if "overwrite an existing image" was read on "Parameters of each layer", there is no teaching

that the foregoing is because of the display manager "providing a signal to the decoder indicating when to overwrite the existing image in the frame buffer". In Figure 6, the "Display Control Section 55" has only two outputs, the "Display Starting Instruction" and the "Display Completion Notice". Assignee respectfully submits that Kono does not teach that the "display starting instruction" and the "display completion notice" are provided to the "image decoding section 52". Accordingly, Assignee also traverses the rejection to claims 1 and 13 for this reason alone.

Moreover, Kono would be inoperable if an image from Kono, frame buffer 53 were immediately overwritten upon transfer to the display unit. Kono [0006] notes that "The B picture is coded by using past and future I picture or past and future P pictures as reference pictures. Therefore, the information of past and future I pictures or P pictures are necessary for decoding the B pictures." Thus, immediately overwriting a picture upon transfer to the display unit would overwrite the information of past I and P pictures that "are necessary for decoding the B pictures." Accordingly, Assignee also traverses the rejection to claims 1 and 13 for this reason alone.

Accordingly, for at least these reasons alone, Assignee respectfully traverses the rejection to claim 1 and 13 and to dependent claims 2-12, and to dependent claims 14-18 and request that Examiner withdraw the rejection.

Claim 7 further recites, "wherein the second processor is off-chip from the integrated circuit". Examiner has indicated that "Kono et al fails to teach wherein the second processor is off-chip from the integrated circuit. However, Jiang et al in the same field of endeavor teaches wherein the second processor is off-chip from the integrated circuit as recited in claim 7 (col. 9, lines 44-47)". Office Action at 6.

Assignee respectfully traverses and notes that Jiang, Col. 9, Lines 44-47 merely teaches, "The display controller 540 retrieves the video data that is to be converted using the 3:2 pulldown technique from the DRAM 190 via the memory interface 520." It is noted that there is no teaching that the "display controller 540" is "off-chip from the integrated circuit". Moreover, Assignee calls Examiner's attention to Figure 5, where the "display controller 540" is a part of the "video stream decoder 180" and clearly not shown "off-chip from the integrated circuit".

Conclusion

For the foregoing reasons, Assignee respectfully submits all of the pending claims are in a condition for allowance, thereby placing the application in a condition for allowance. It is believed that there is no fee associated with any of the actions requested herein. To the extent that there is any fee associated with any actions requested herein, the Commissioner is requested to charge such fee to deposit account 13-0017.

RESPECTFULLY SUBMITTED



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